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Inventor:	Barrie Gilbert		
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Title:	SQUARING CELLS AND MULTIPLIERS USING SUMMED EXPONENTIALS		

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub-Class
ML	___	6,052,349	04/2000	Okamoto	369	47.26
ML	___	5,714,911	02/03/1998	Gilbert	331	57
ML	___	5,684,431	11/04/1997	Gilbert et al.	330	254
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Exam Init	Ref	Document Number	Publication Date	Country	Name
ML	___	EP 0 665 639 AI	08/95	Europe	Gilbert

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ML	___	Gilbert, Barrie; Wideband Negative-Current Mirror; *Electronics Letters*; Vol. 11, No. 6, March 20, 1975; pages 126-127
ML	___	Gilbert, Barrie; *Current-mode Circuits From A Translinear Viewpoint: A Tutorial*; Analogue IC design: the current mode approach; Peter Peregrinus, Ltd., London, United Kingdom; 1990 (pages 11-91)

Examiner: _____ NW
Date Considered: _____ 11/8/04